

Figure 1

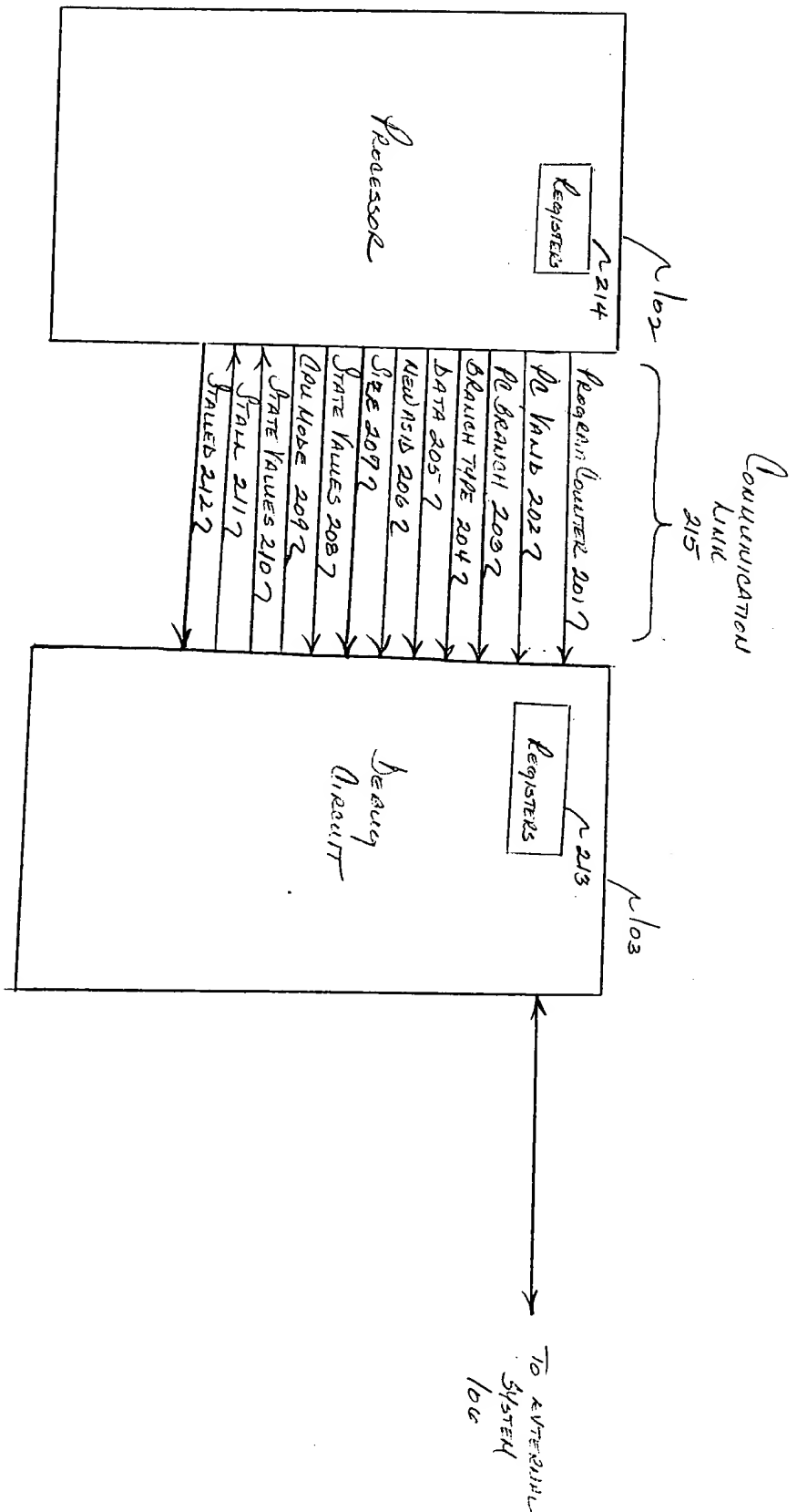


Figure 2

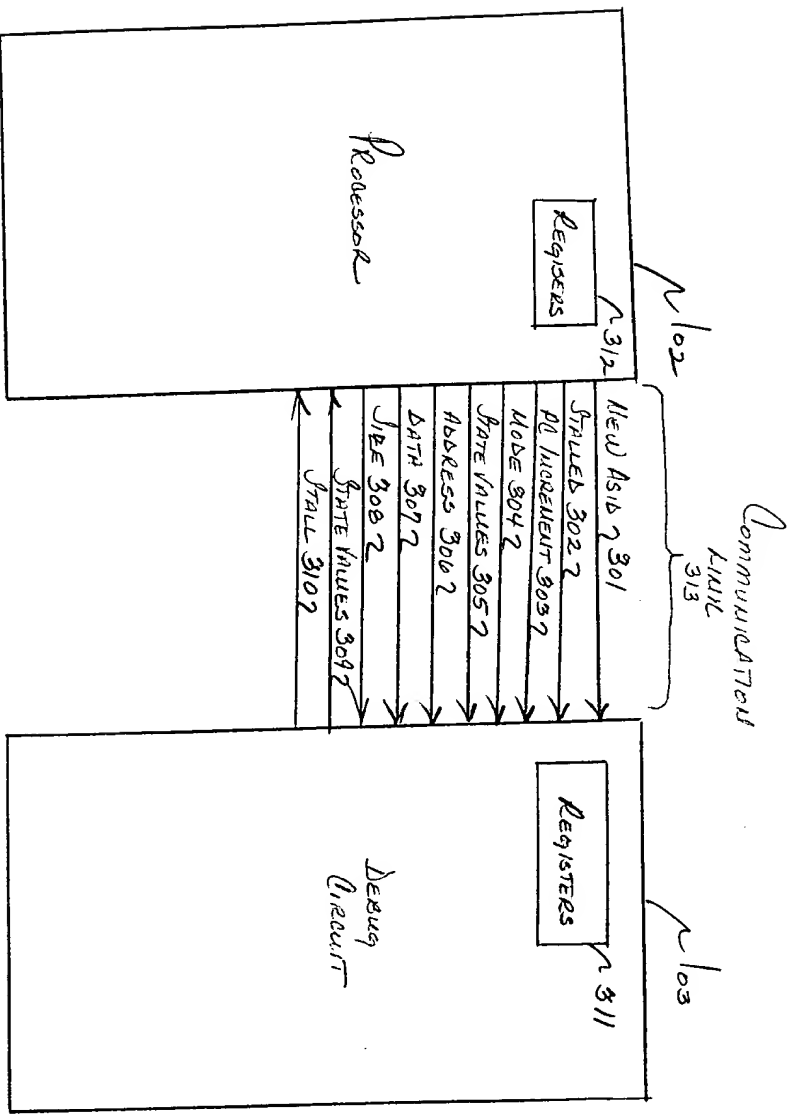


Figure 3

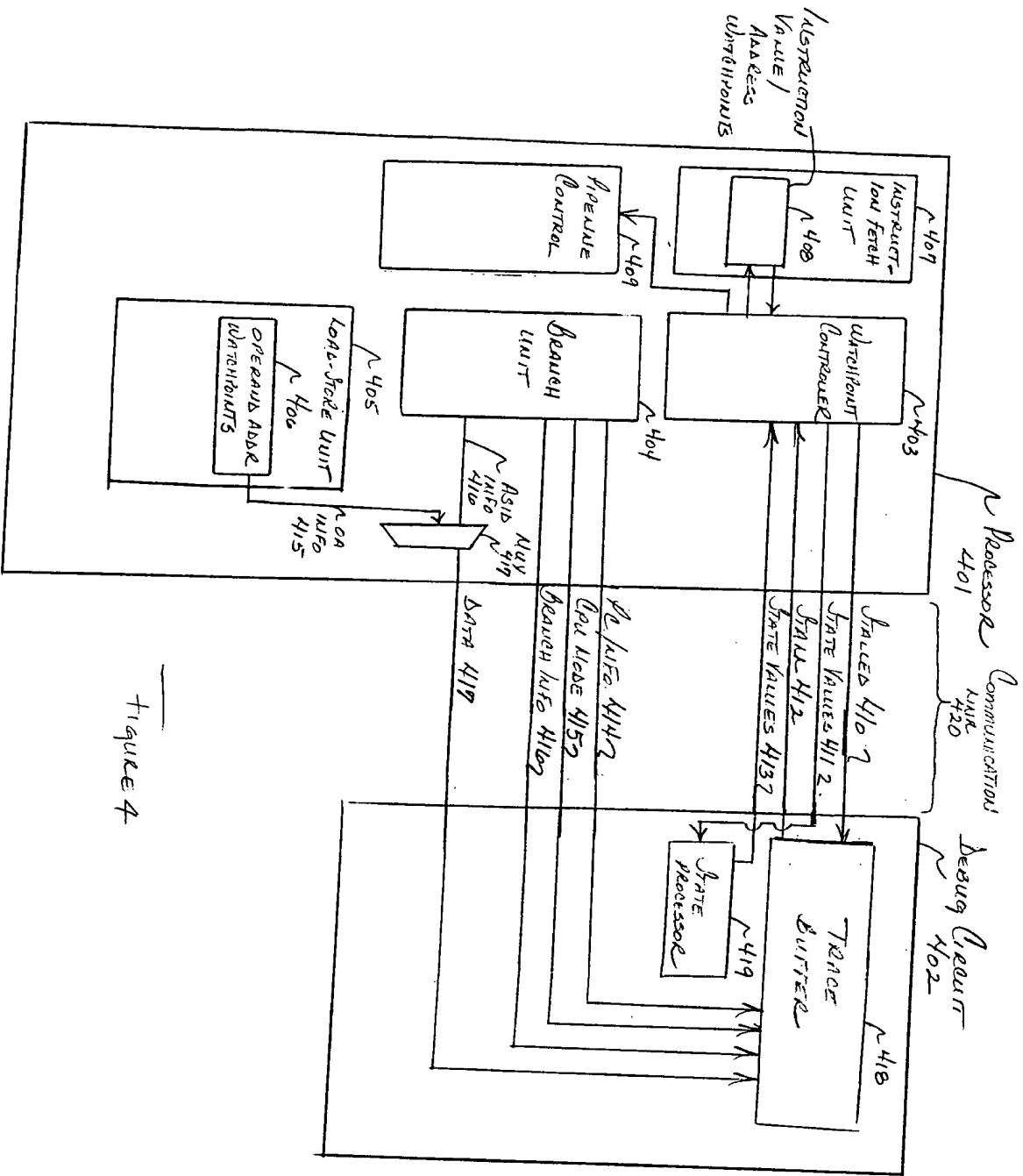


Figure 4

00441792-100199

Processor
501

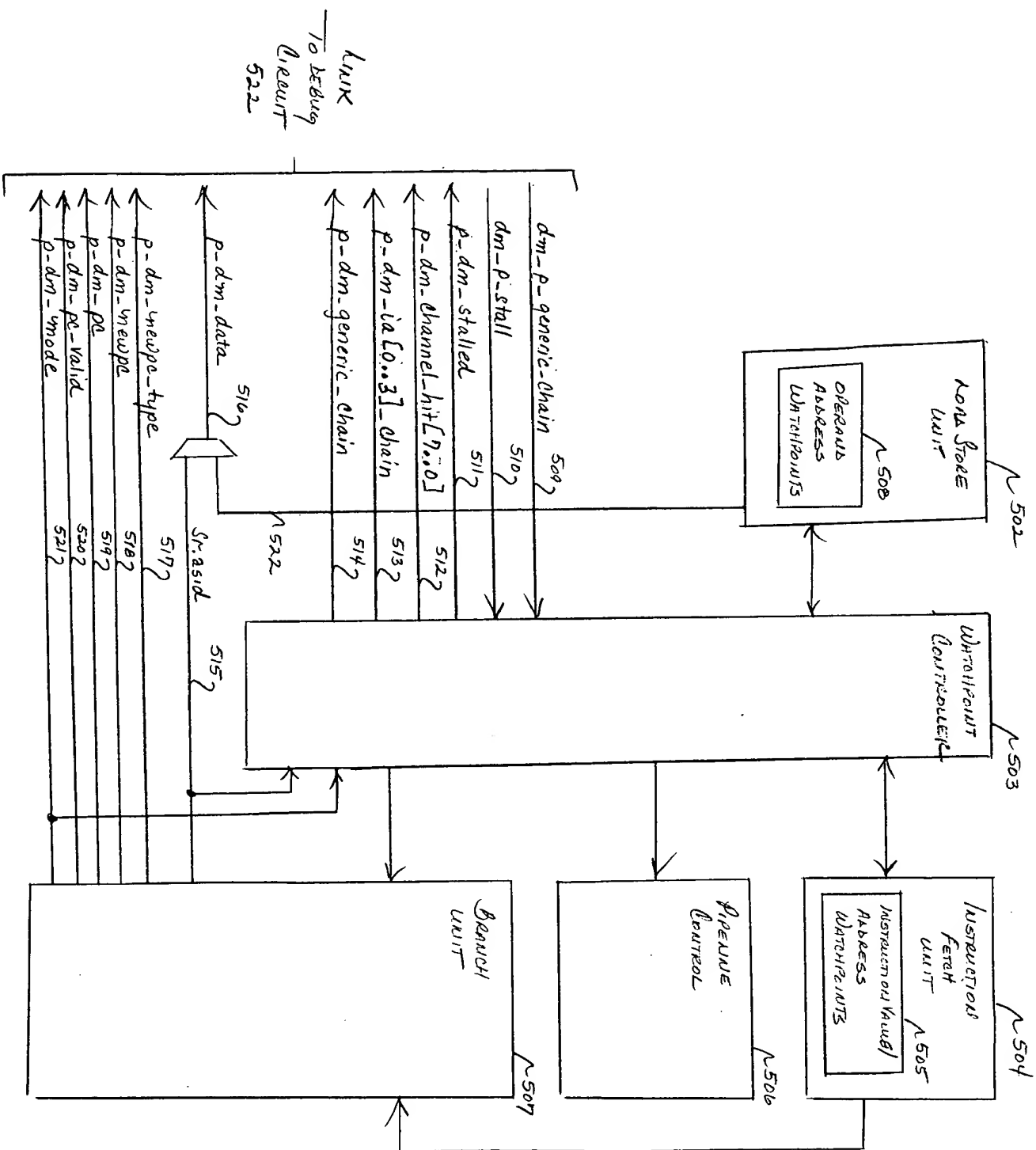
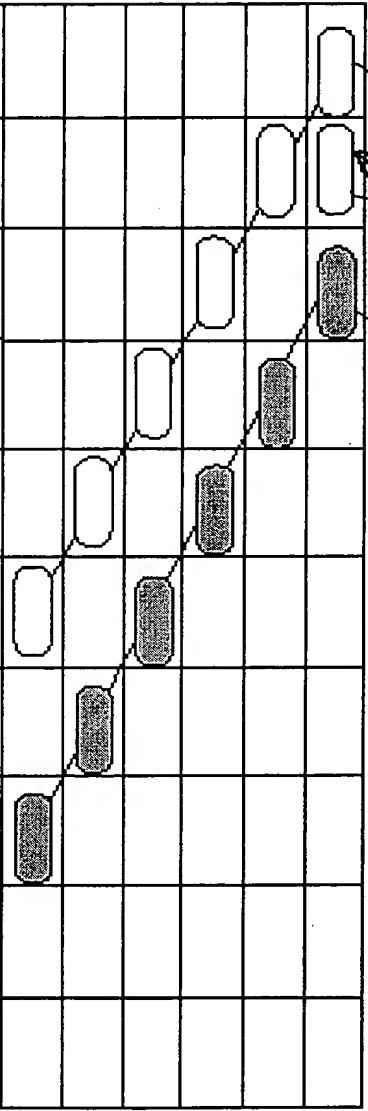


Figure 5

F
D
E1
E2
E3
W

BLINK(I) I' discarded
ADD (T) 602 603



P_{dm_pc}
P_{dm_pc_valid}
P_{dm_newpc}
P_{dm_newpc_type}

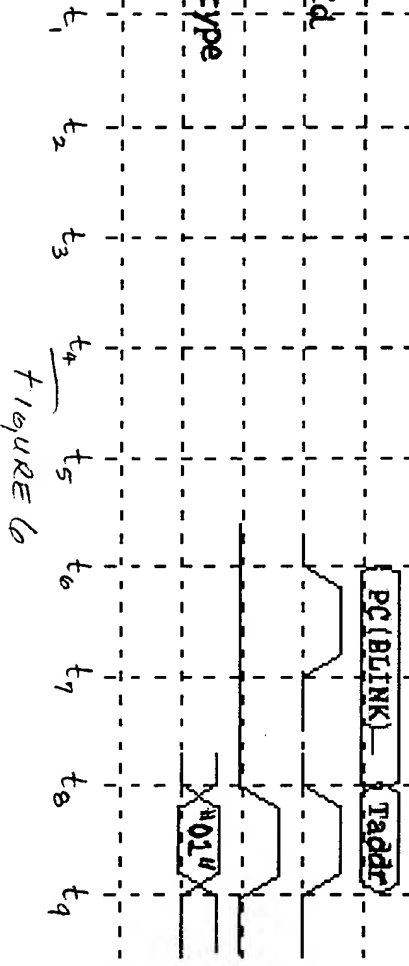


FIGURE 6